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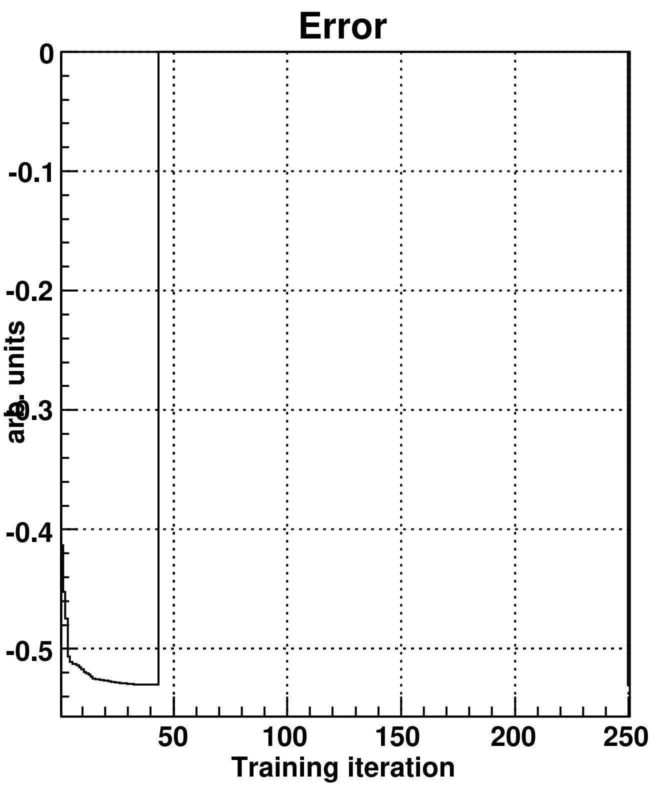

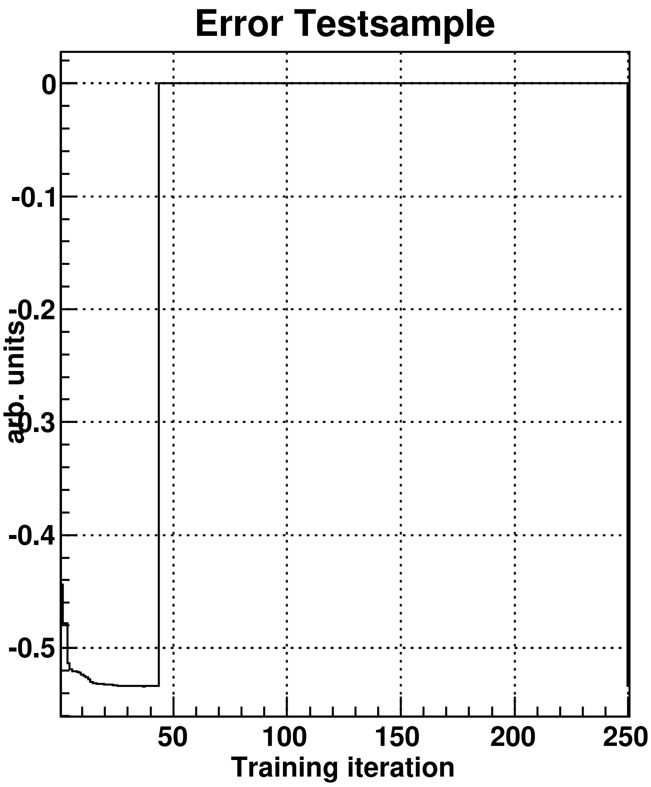

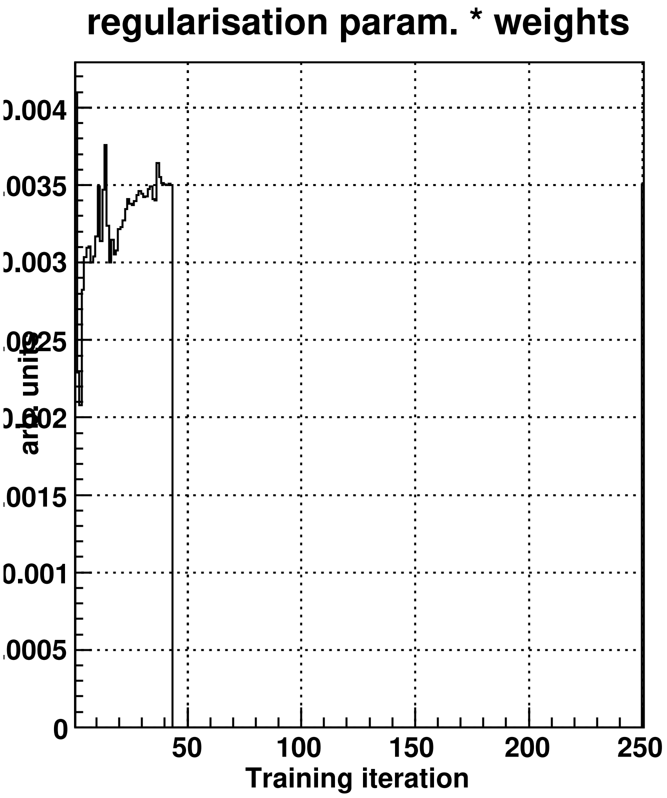

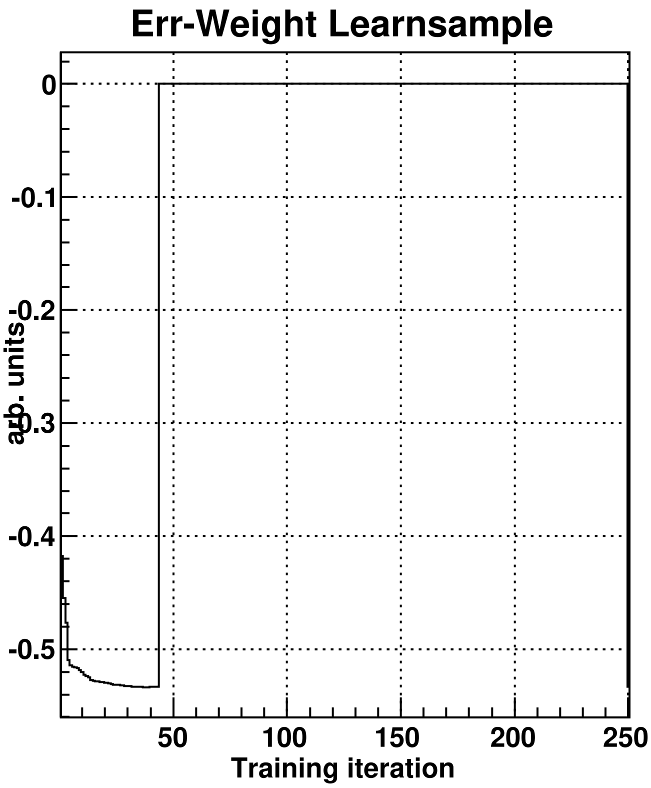

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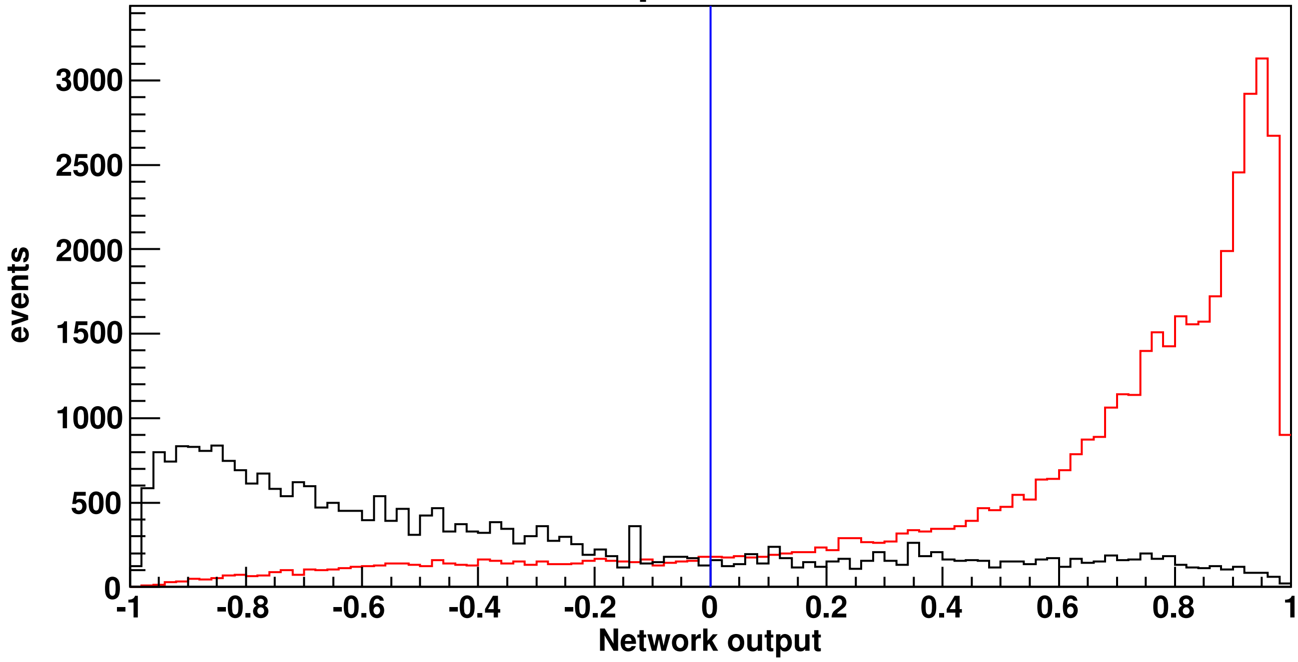
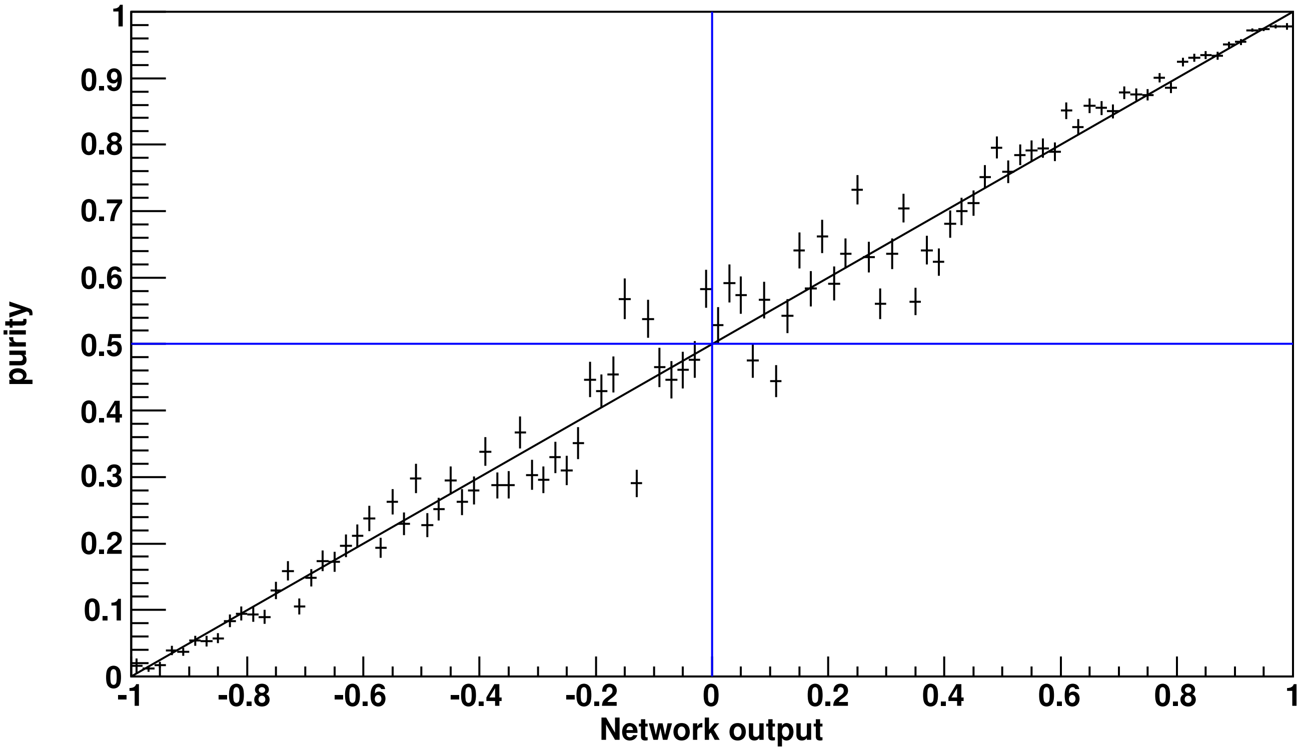
Output Node 1





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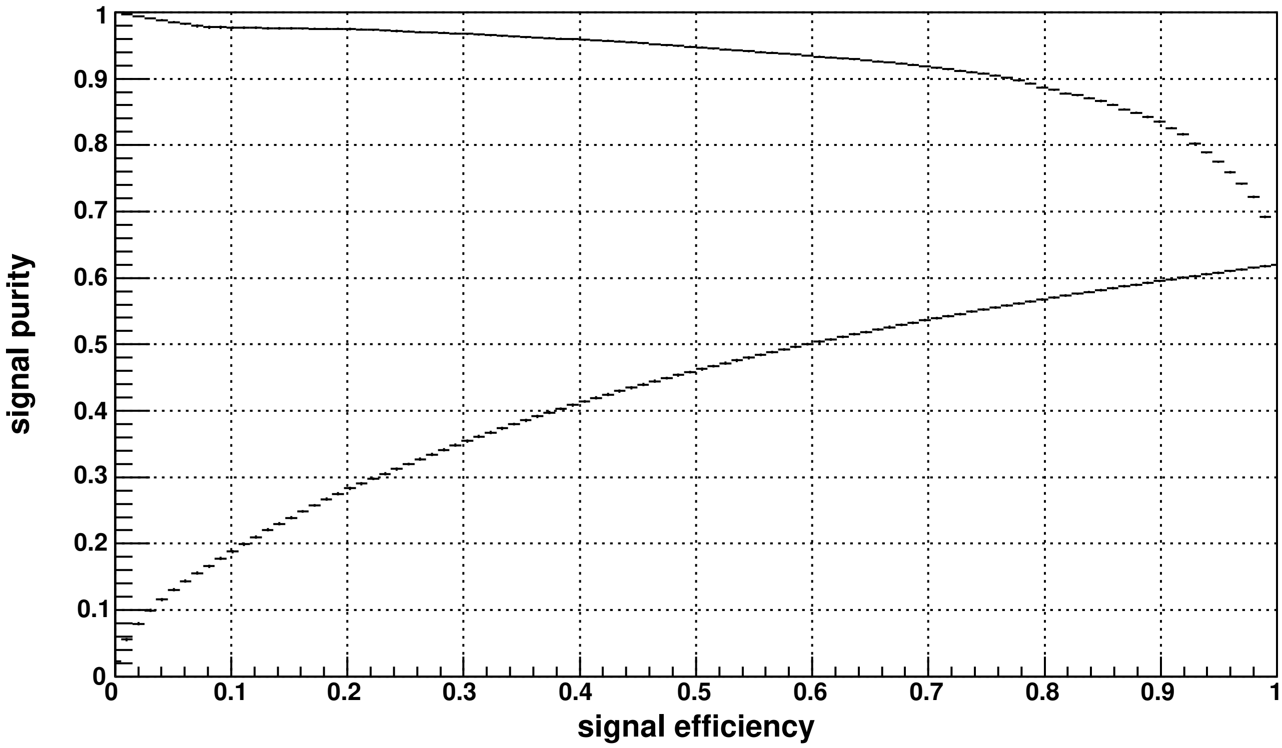

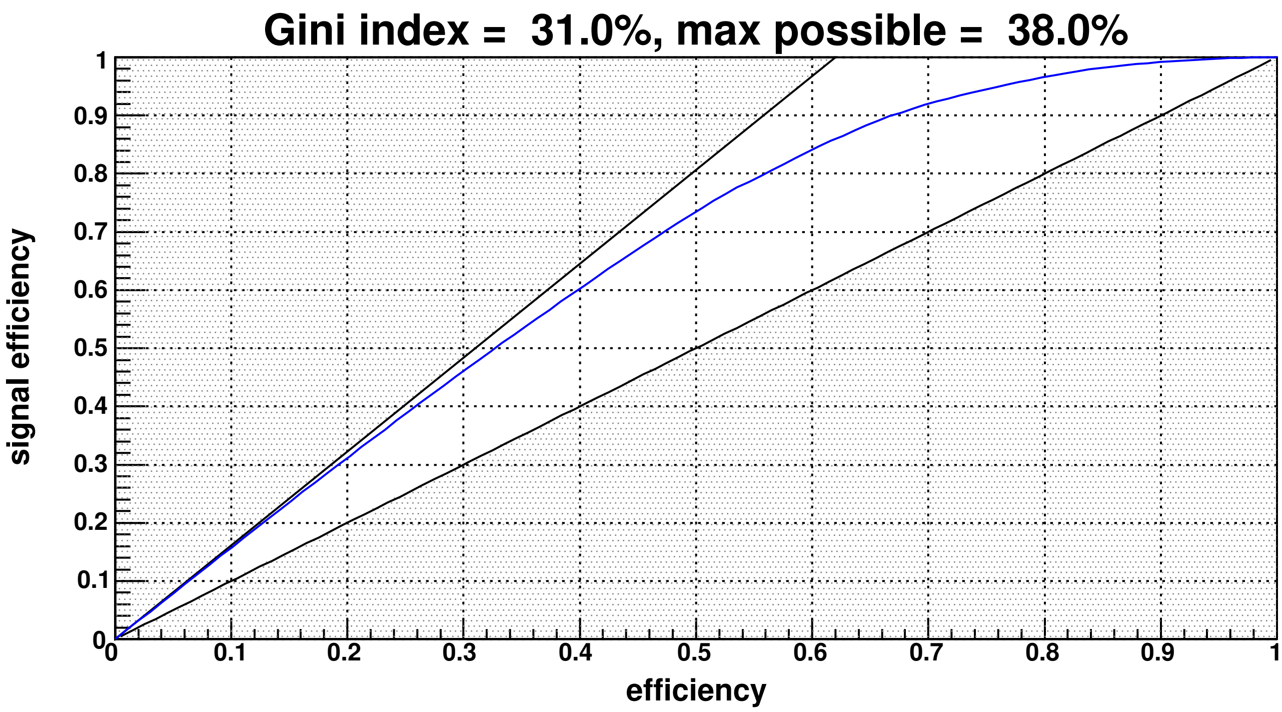

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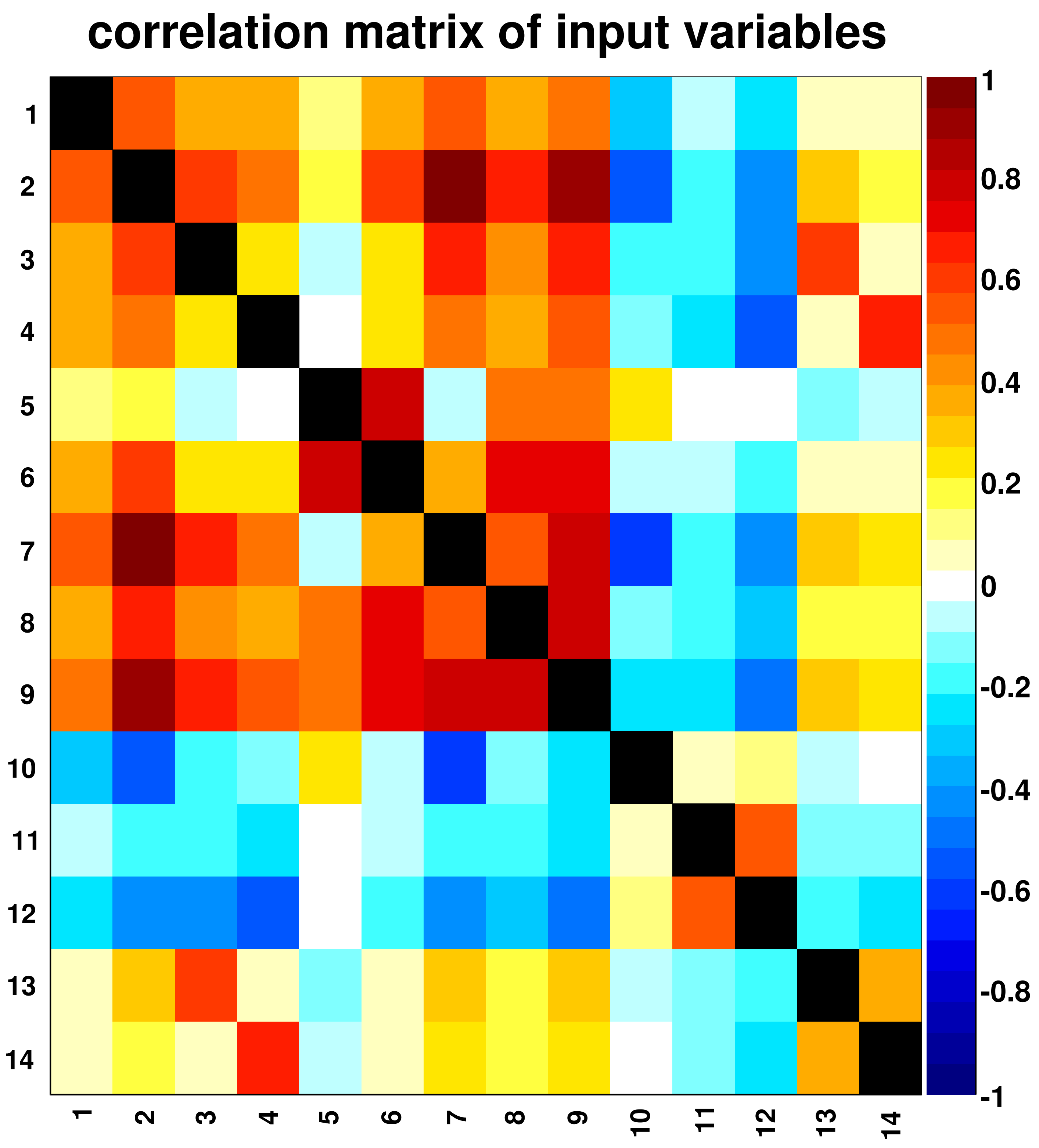

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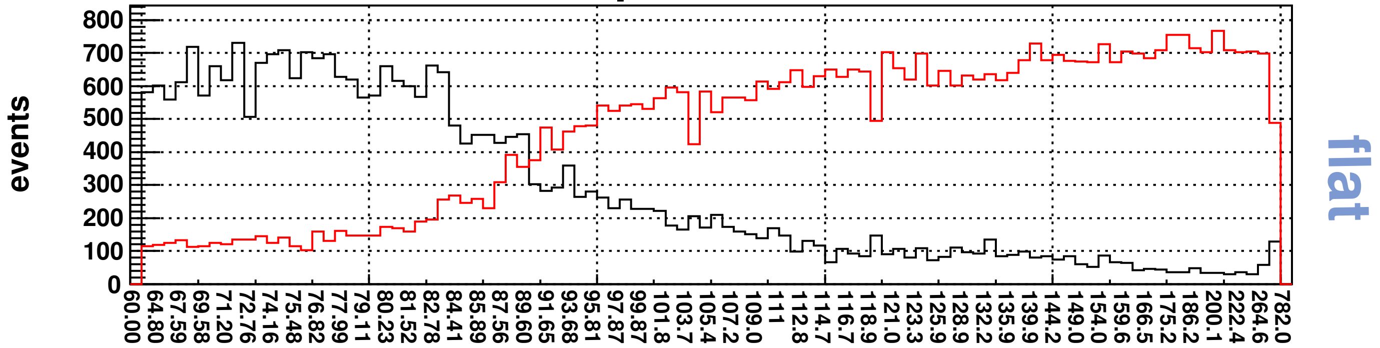
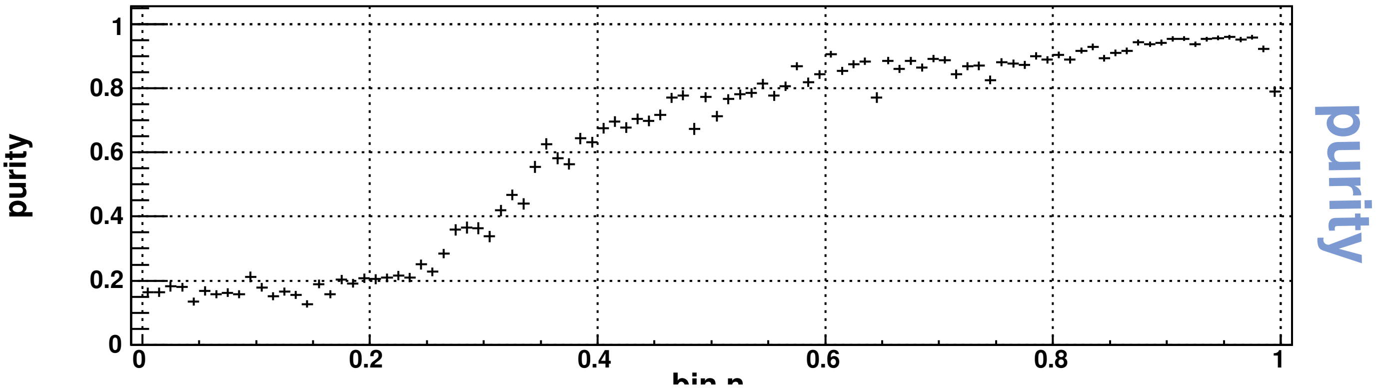
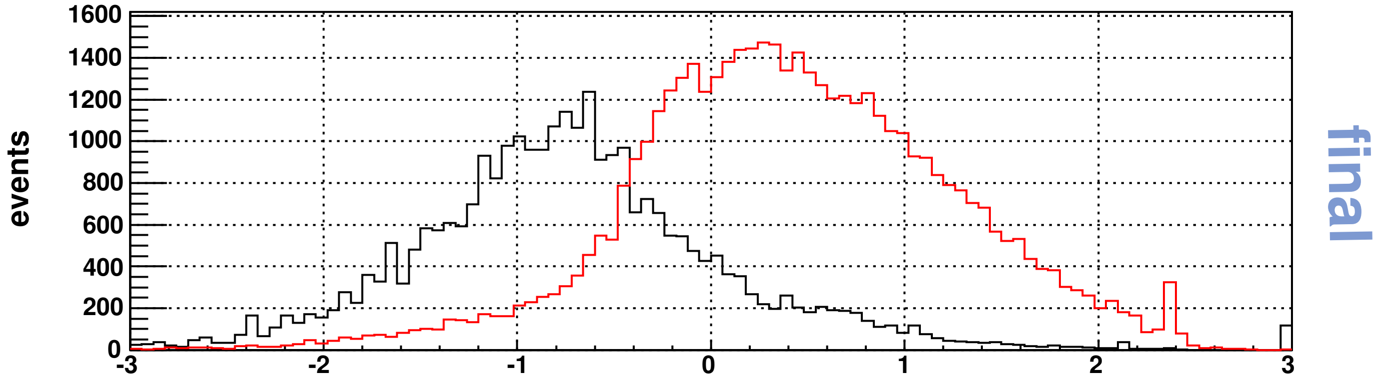
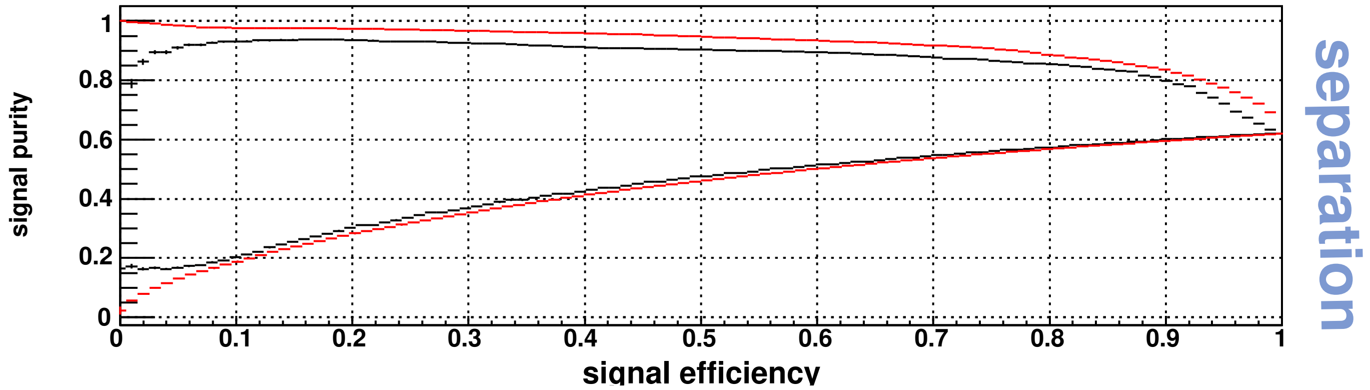
input node 2









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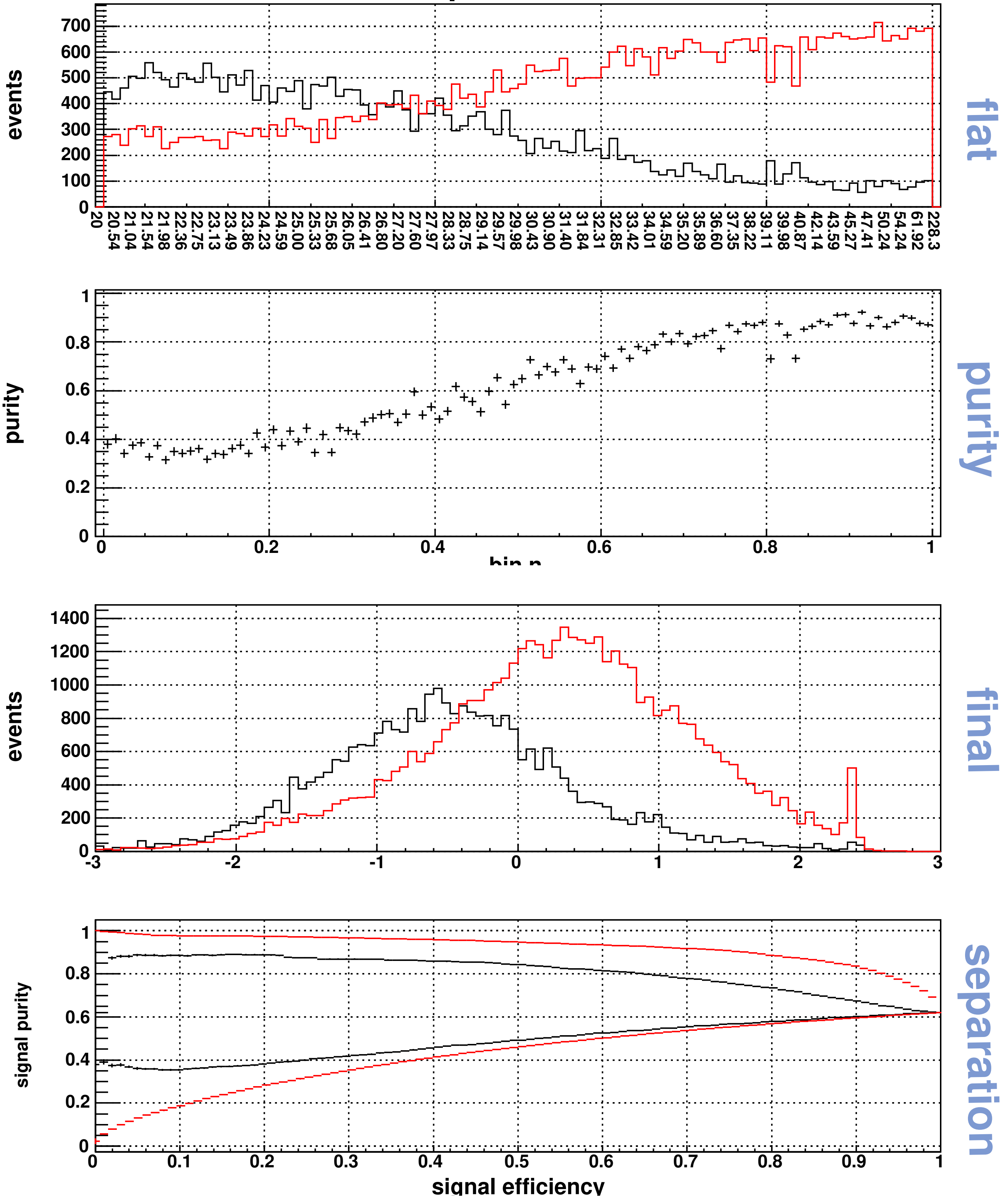
input node 3



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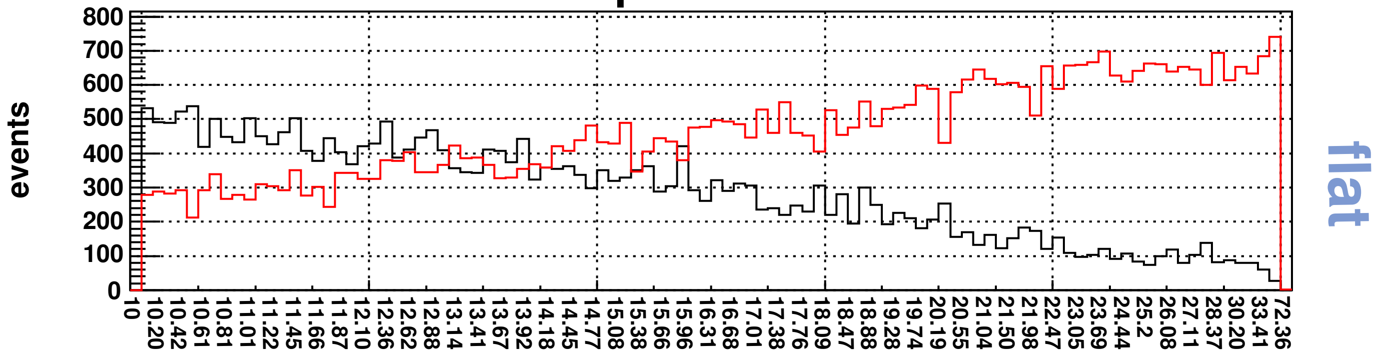
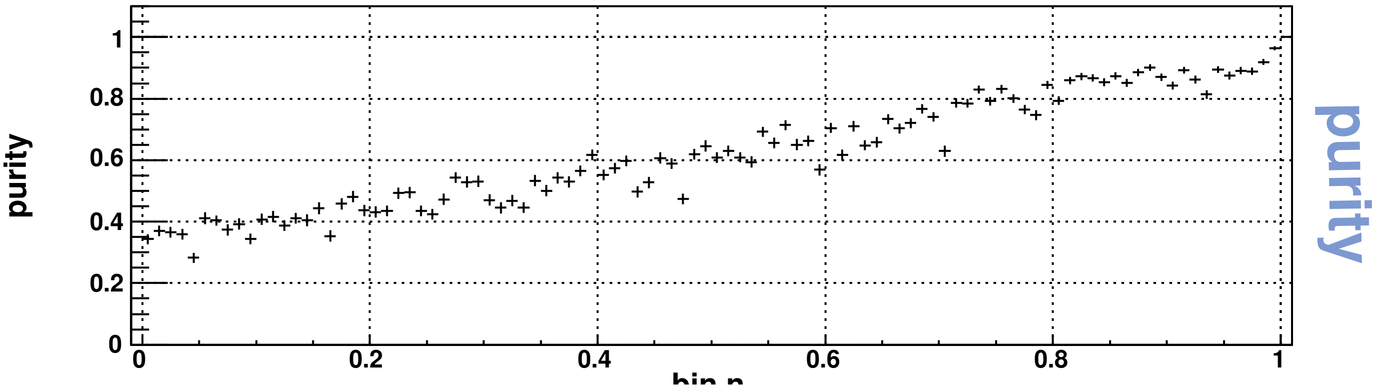
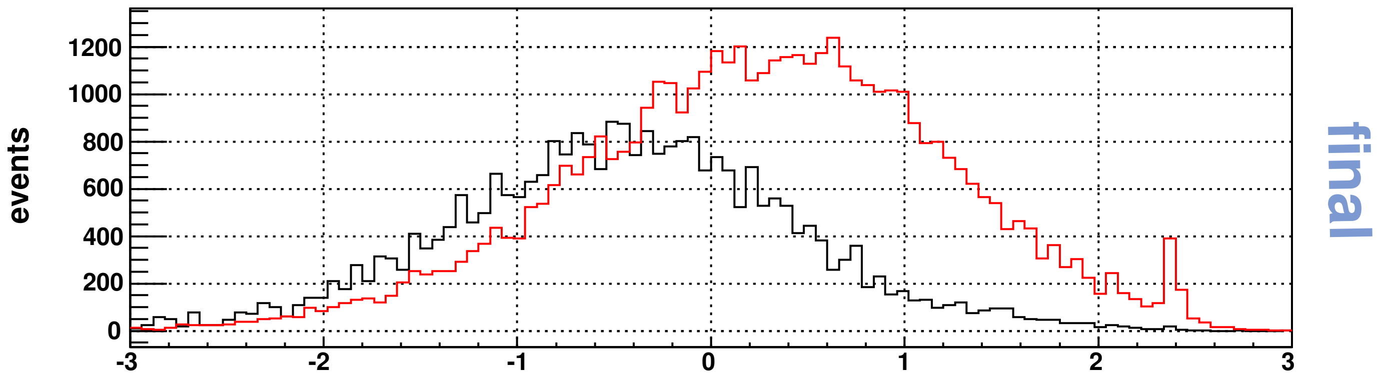
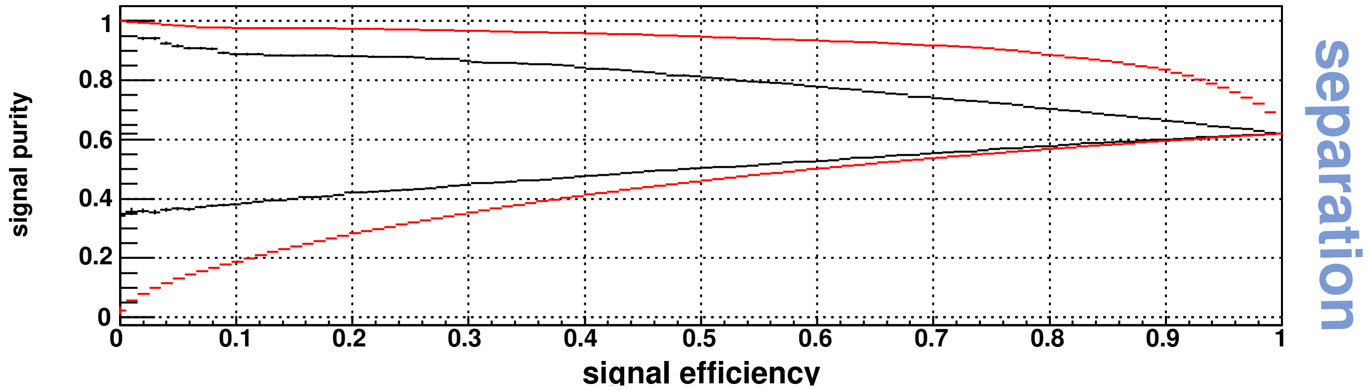
input node 4









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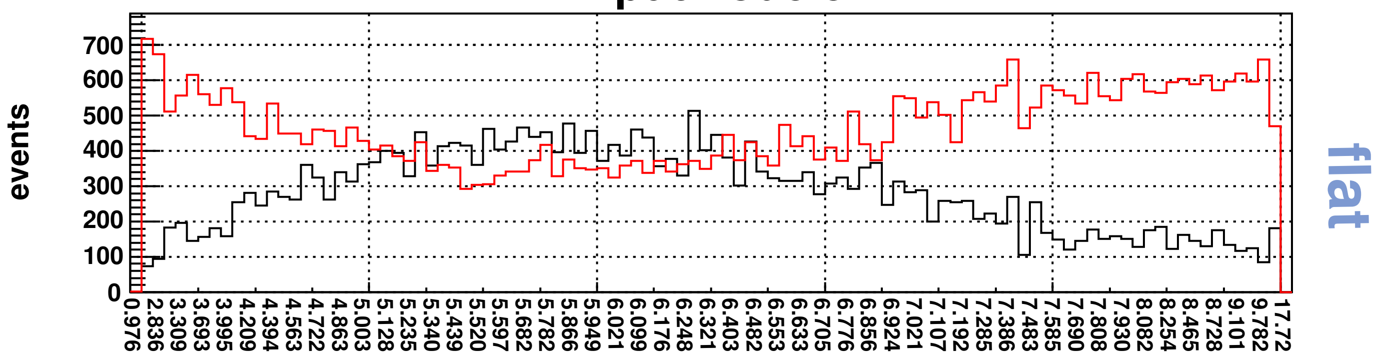
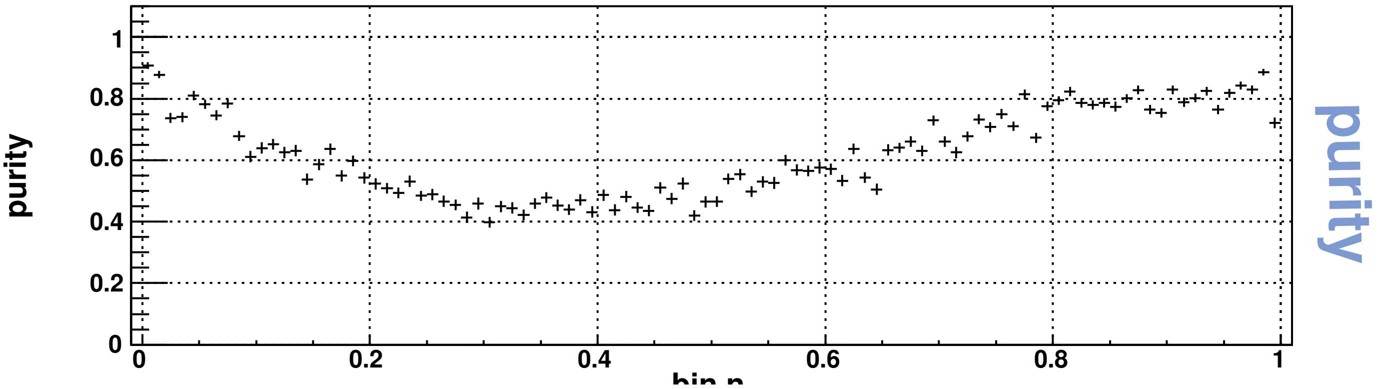
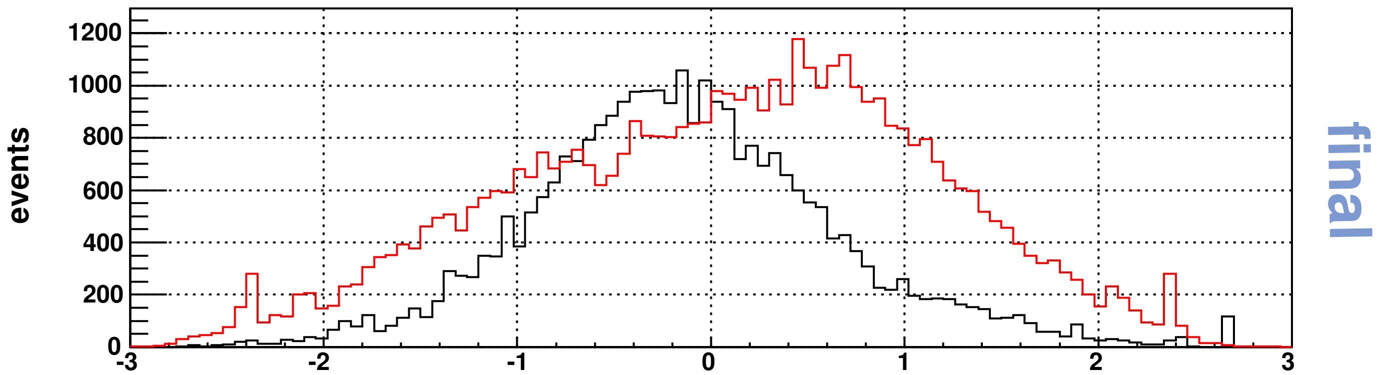
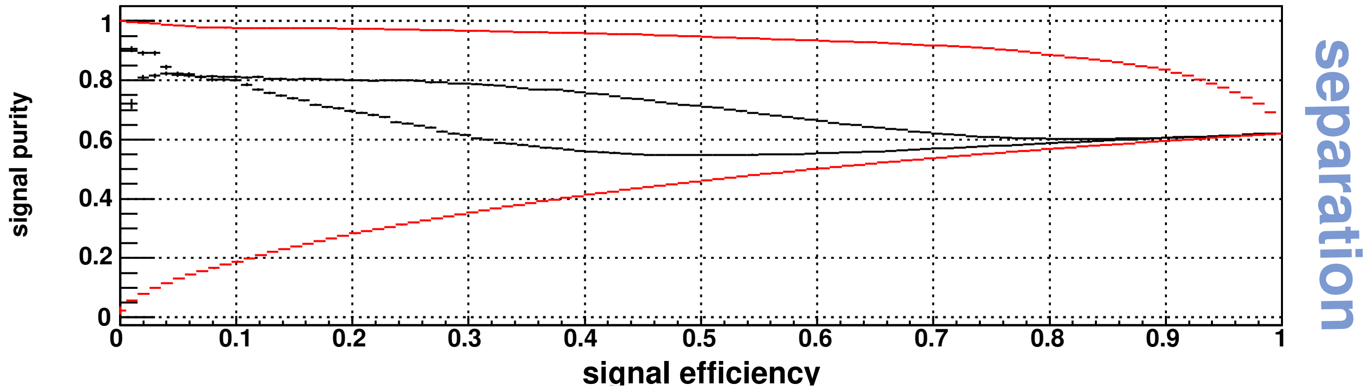
input node 5









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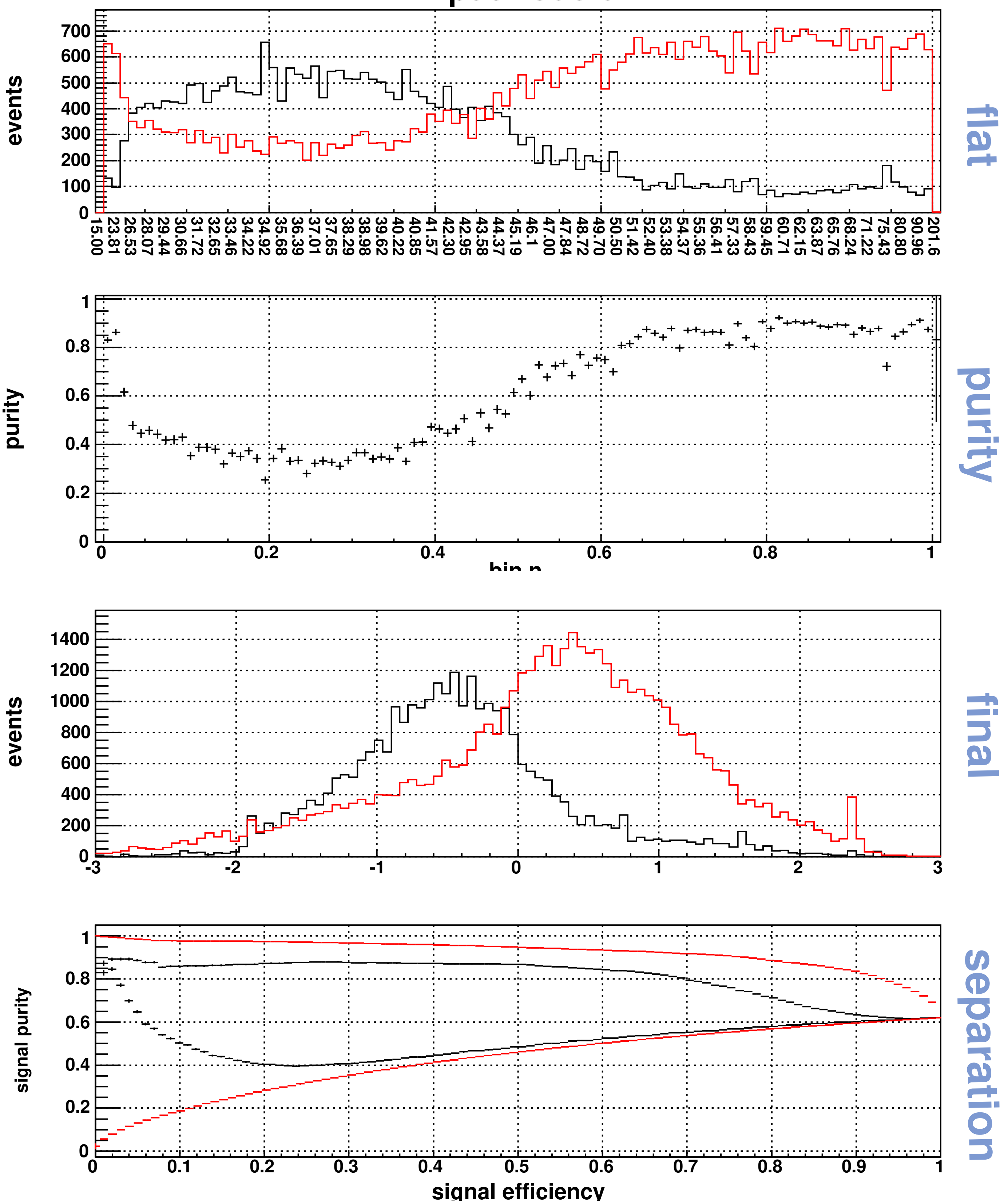
input node 6



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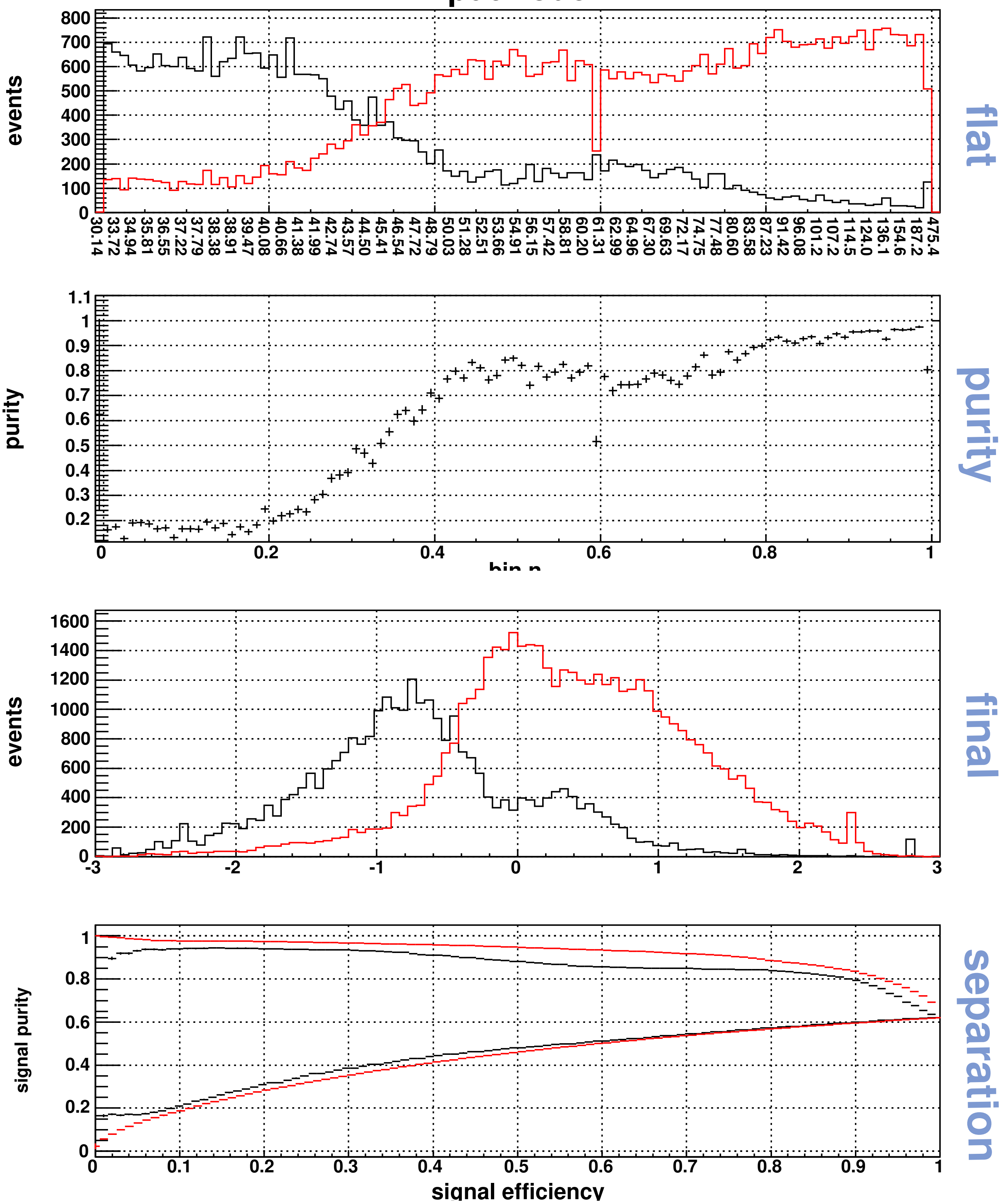
input node 7



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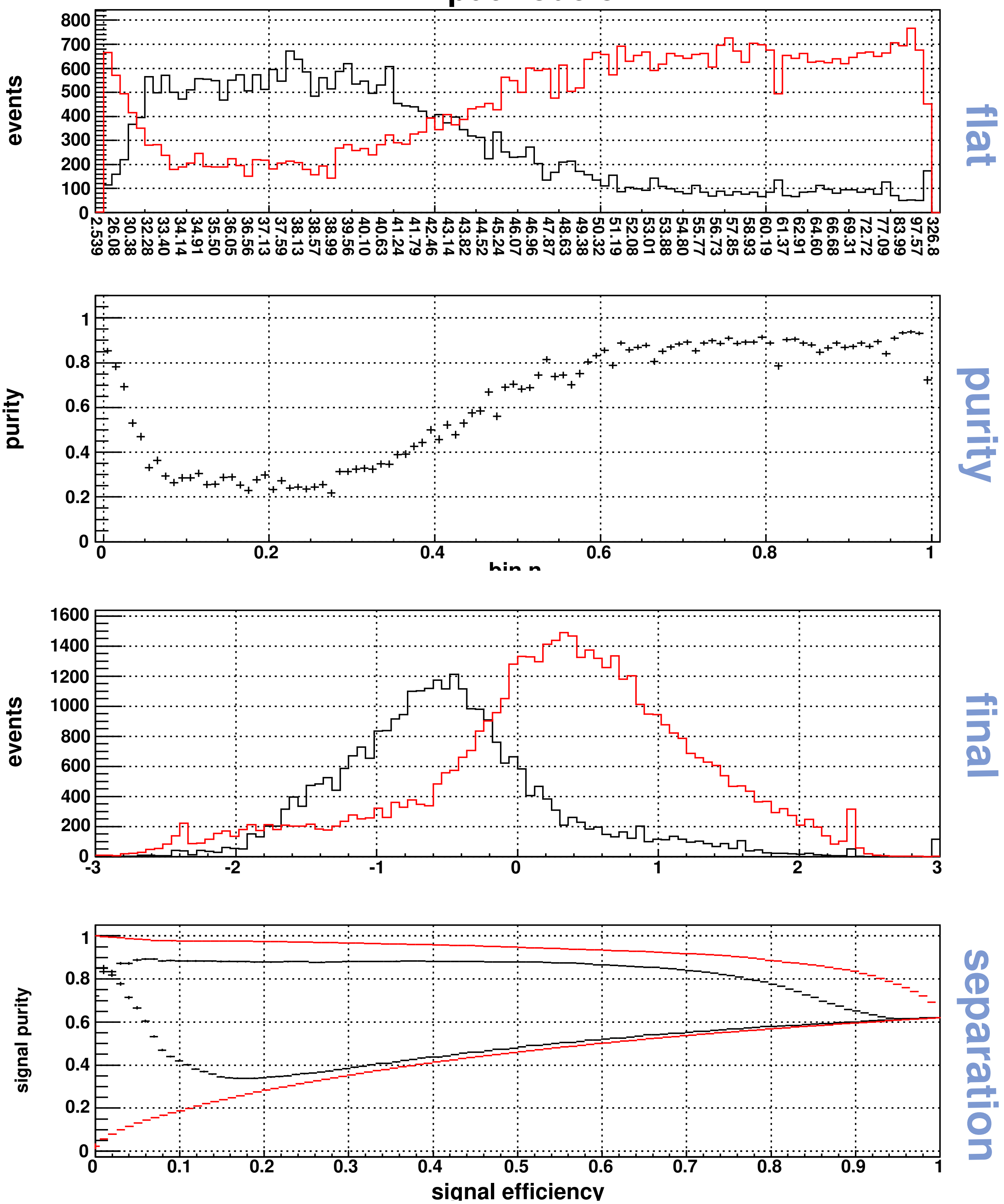
input node 8



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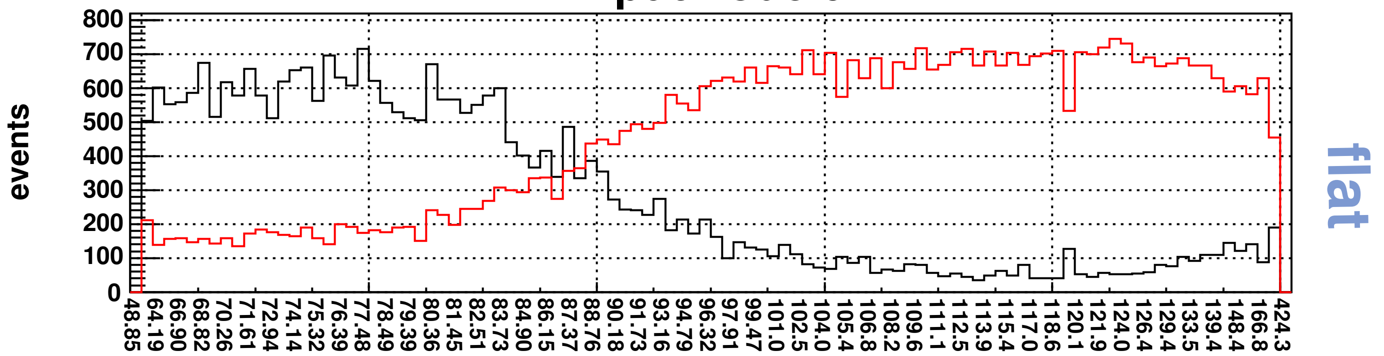
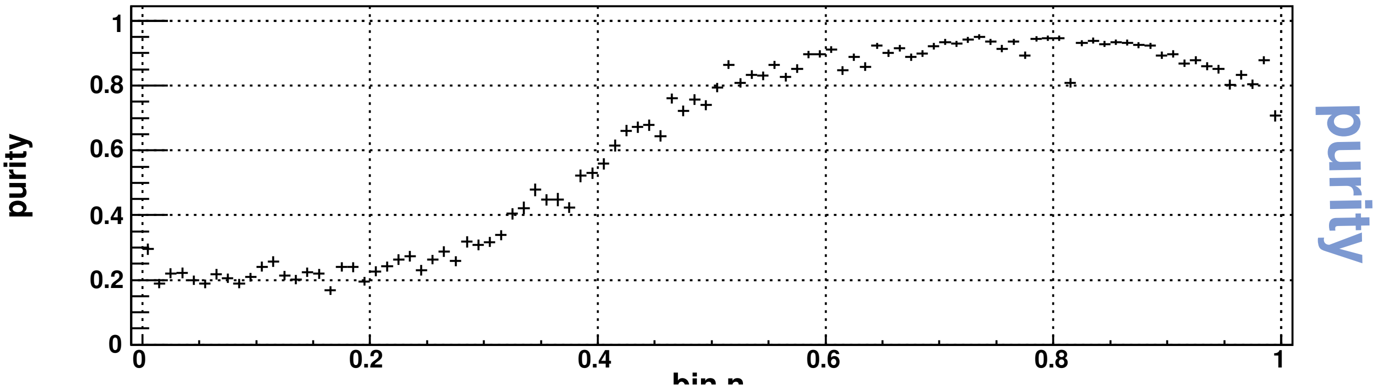
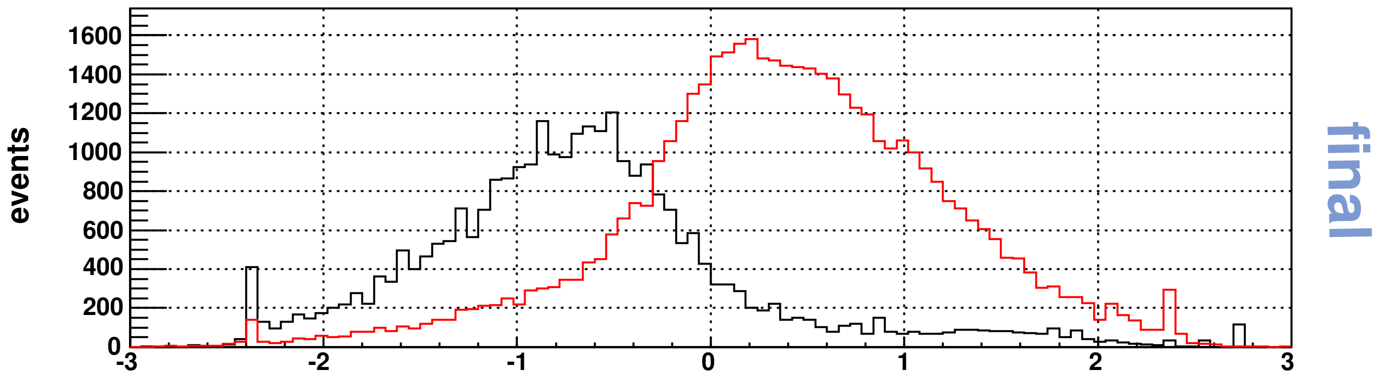
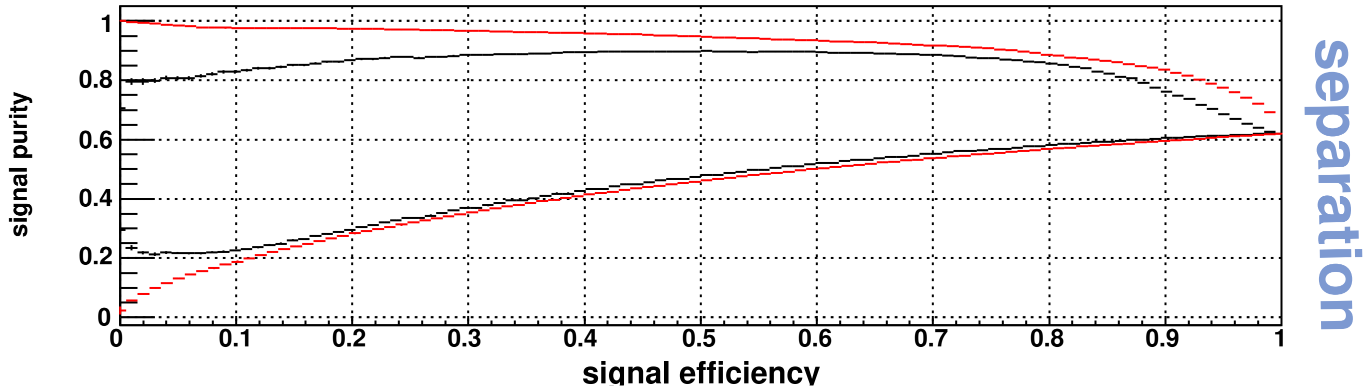
input node 9









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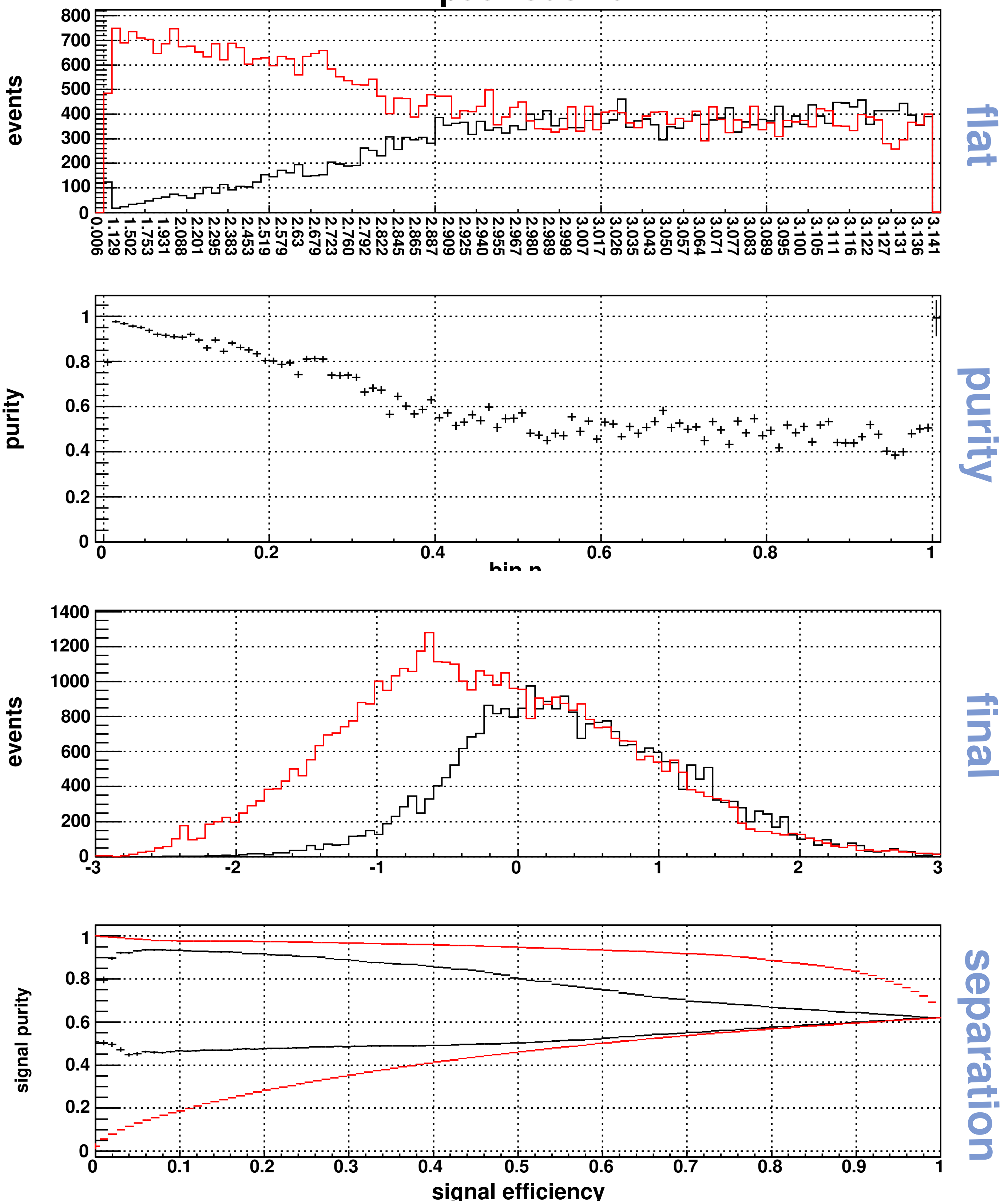
input node 10



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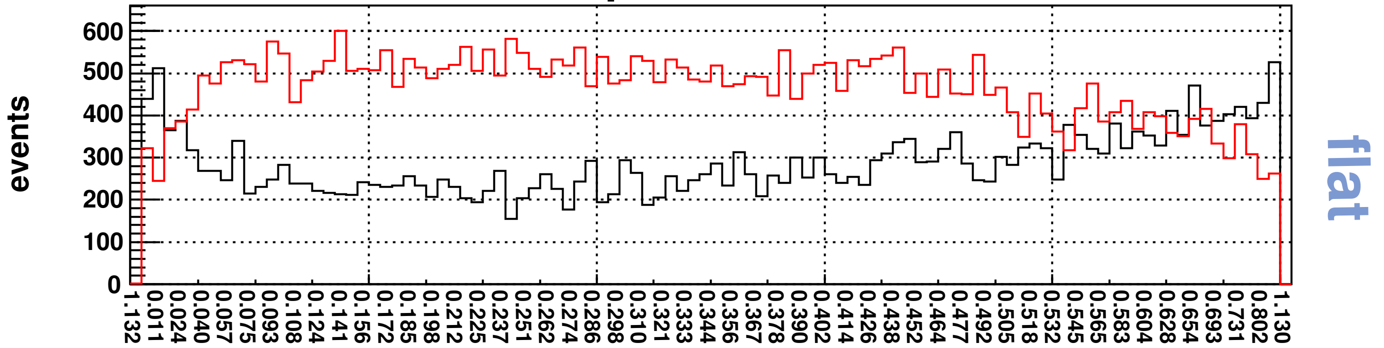
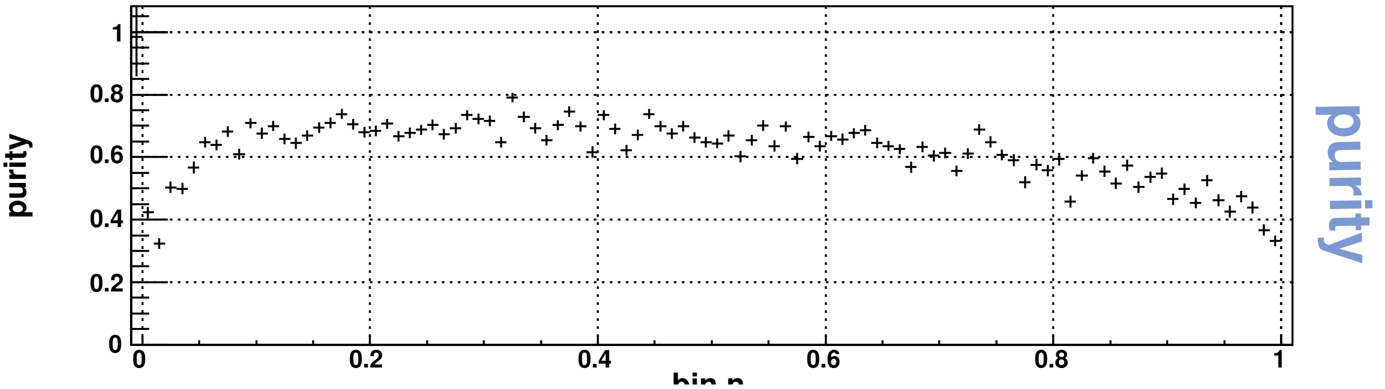
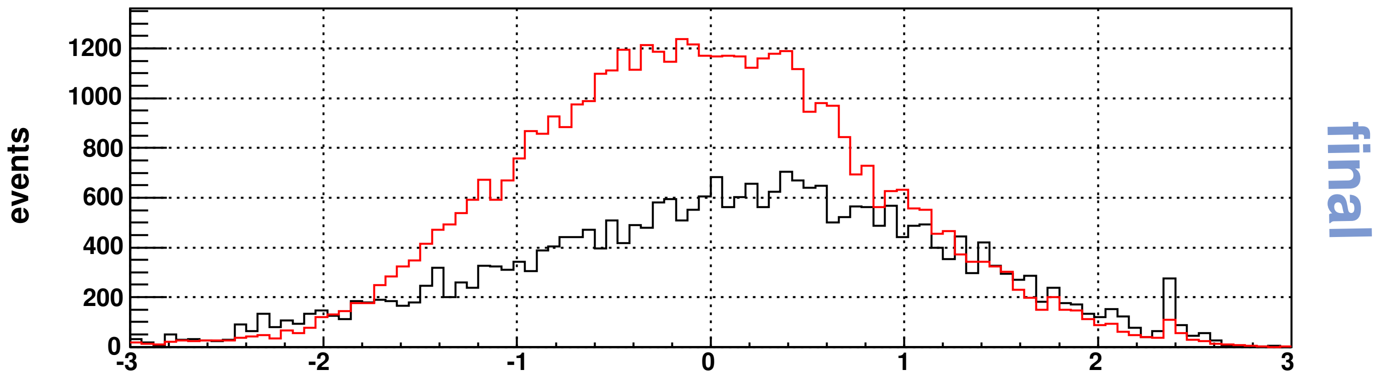
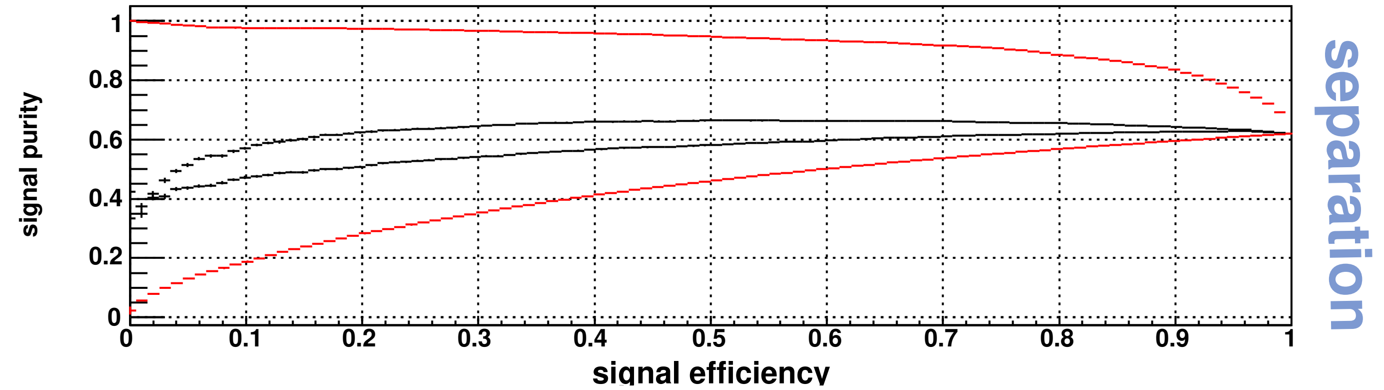
input node 11









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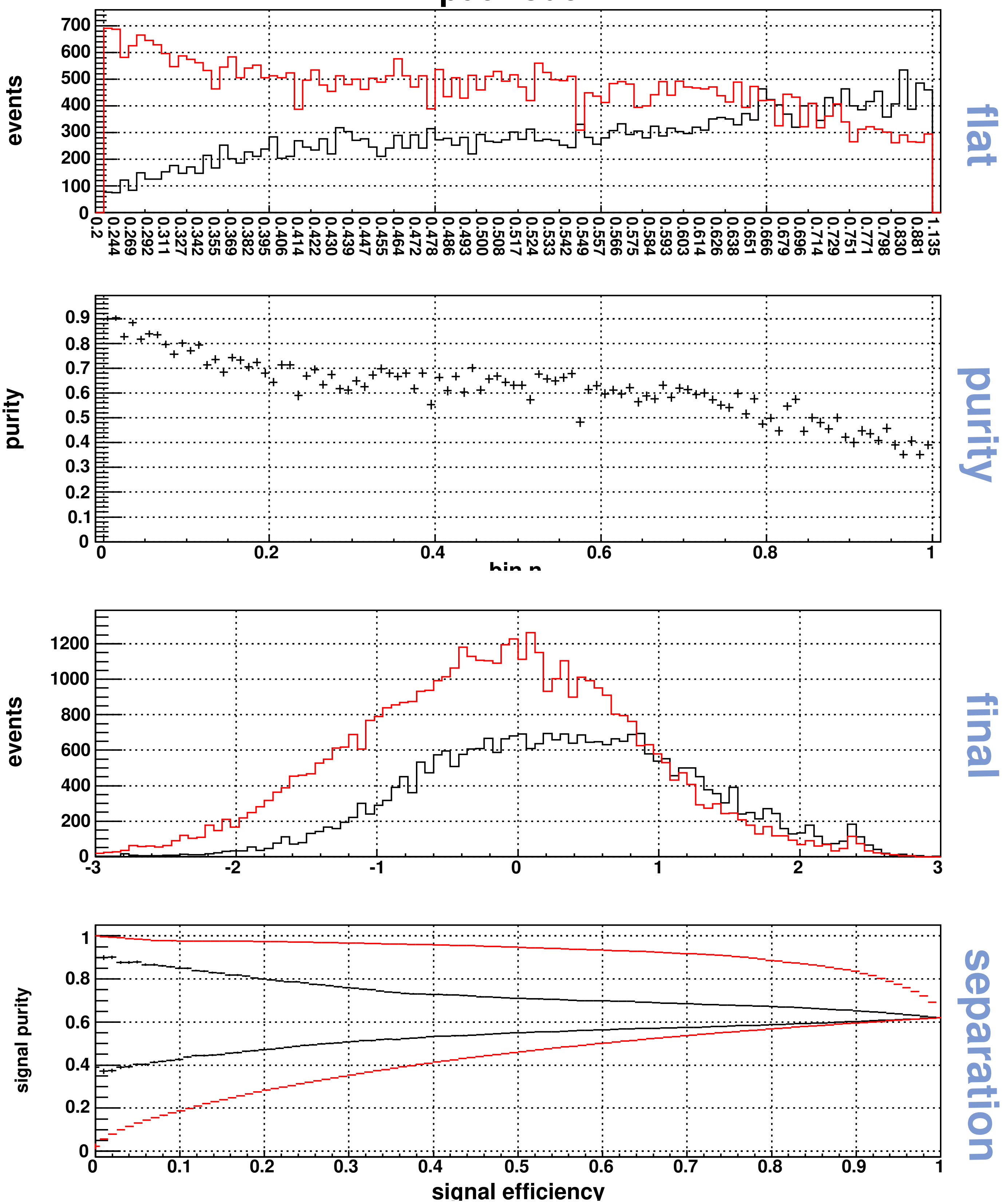
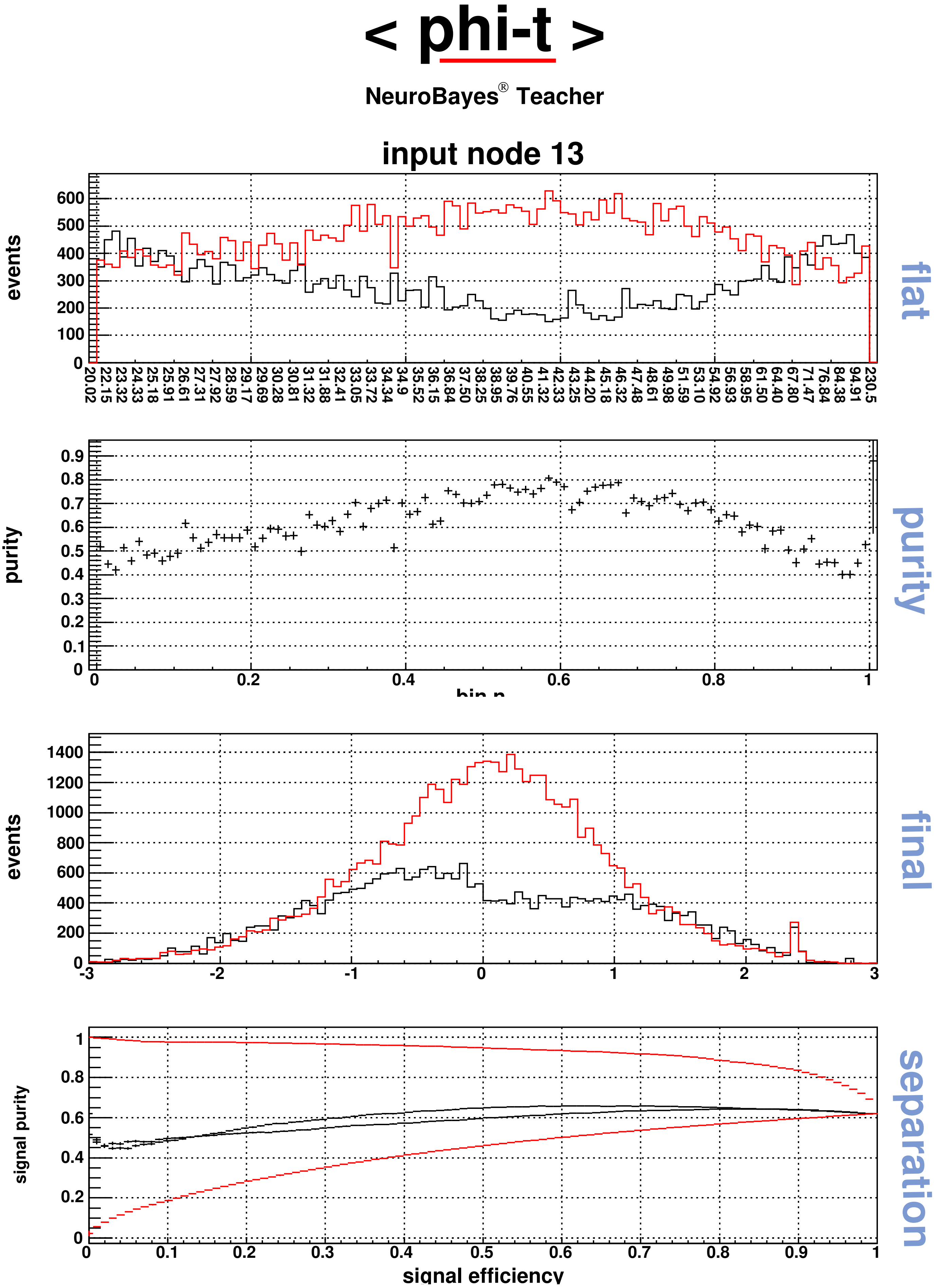
input node 12





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input node 13
events
flat
purity
bin n
events
final
signal purity
signal efficiency
separation

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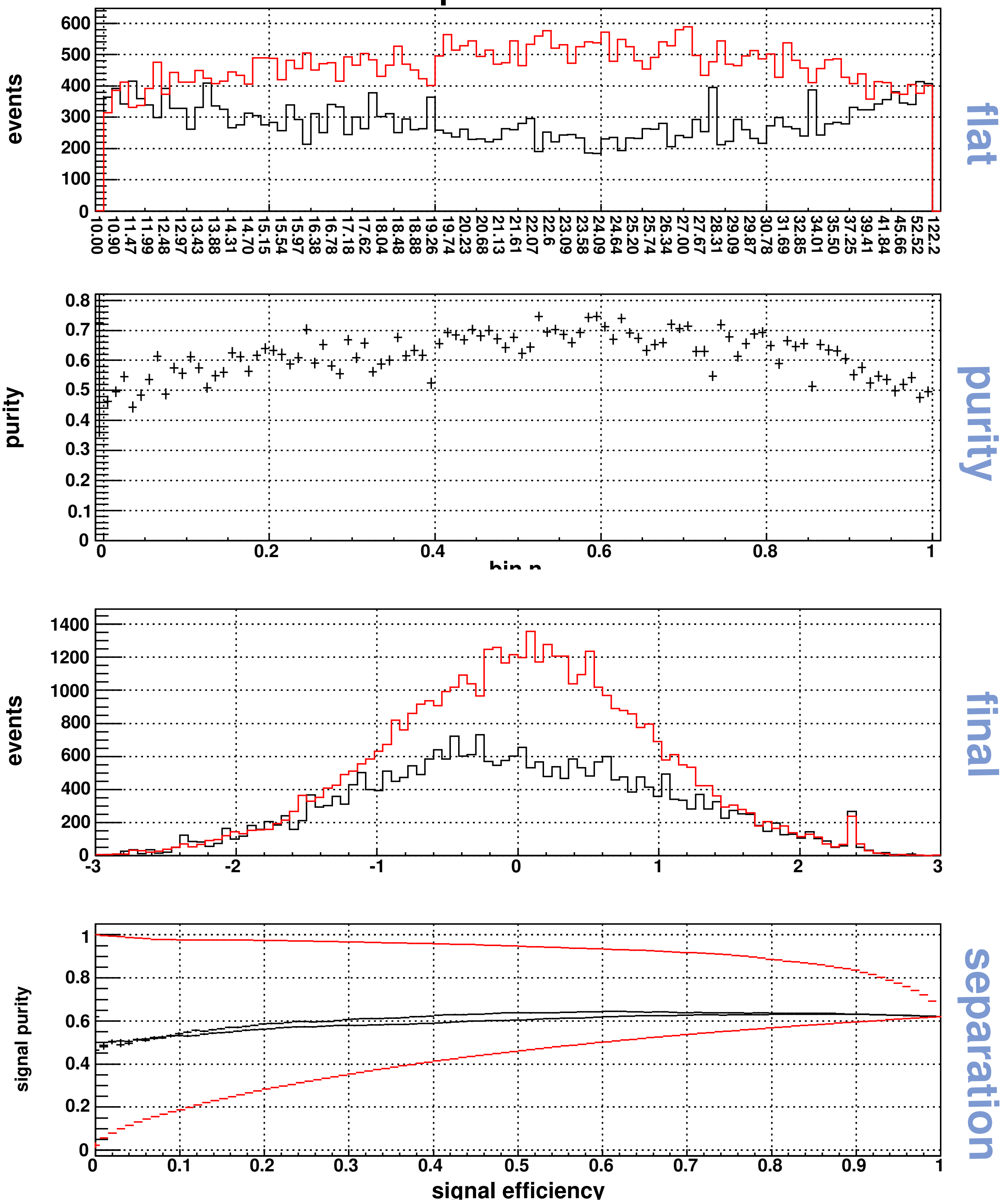
input node 14



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